### TERMINATION OF PROBABILISTIC CONCURRENT PROGRAMS (Extended Abstract)

Sergiu Hart, Micha Sharir Tel Aviv University

Amir Pnueli Weizmann Institute of Science

#### Abstract.

The asynchronous execution behavior of several concurrent processes, which may use randomization, is studied. Viewing each process as a discrete Markov chain over the set of common execution states, we give necessary and sufficient conditions for the processes to converge almost surely to a given set of goal states, under any fair, but otherwise arbitrary schedule, provided that the state space is finite. (These conditions can be checked mechanically.) An interesting feature of the proof method is that it depends only on the topology of the transitions and not on the actual values of the probabilities. We also show that in our model synchronization protocols that use randomization are in certain cases no more powerful than deterministic protocols. This is demonstrated by (a) Proving lower bounds on the size of a shared variable necessary to ensure mutual exlusion and lockout-free behavior of the protocol; and (b) Showing that no fully symmetric 'randomized' protocol can ensure mutual exclusion and freedom from lockout.

#### 0. Introduction.

Randomization has proven to be a very useful tool in the construction of certain algorithms for parallel processes, which behave 'better' than their deterministic counterparts, by using less shared memory, having relatively simpler structure, and in certain cases accomplishing goals that deterministic algorithms provably can not accomplish. The price that one usually has to pay in using such a probabilistic algorithm is that certain properties that are required from the algorithm will happen with probability 1, but not necessarily with certainty. Recently published algorithms of this sort include synchronization protocols [Ral], choice coordination [Ra2], a symmetric distributed solution to the dining philosophers' problem [LR],

Permission to copy without fee all or part of this material is granted provided that the copies are not made or distributed for direct commercial advantage, the ACM copyright notice and the title of the publication and its date appear, and notice is given that copying is by permission of the Association for Computing Machinery. To copy otherwise, or to republish, requires a fee and/or specific permission. a probabilistic implementation of a common resource allocation scheme [FR], and various algorithms in symmetric distributed systems [IR].

The problem that one encounters in designing such an algorithm is in showing that the algorithm is correct in a probabilistic sense. This is quite a tedious task, since one must consider all possible sequences of execution steps taken by the processes and show that none of them can prevent a certain desired event from happening with probability 1. To quote Lehmann and Rabin in [LR],

"The realm of proofs of correctness for concurrent processes is not well known. As the reader will realize, proofs of correctness for probabilistic distributed systems are extremely slippery."

Our aim in this paper is to shed some light on this unexplored realm. In particular, assuming that each process has only finitely many states, we will give necessary and sufficient conditions for certain probabilistic properties of a parallel algorithm to hold with probability 1. Moreover, these conditions can be tested algorithmically, given the "atomic" structure of each process. That is, we present a fully mechanical decision procedure of certain probabilistic properties, including, e.g., freedom from deadlock and from lockout.

We will also be concerned with comparing the capabilities of probabilistic synchronization protocols with those of deterministic protocols. We will provide in Section 2 two instances in which the probabilistic approach is provably not more powerful than the deterministic approach. These instances are: (a) Achieving mutual exclusion and freedom from individual lockout by using a shared variable v on which indivisible 'test-and-set' operations are allowed (here the size of v must be  $\Omega(N)$  in both cases, where N is the number of processes). (b) Achieving those properties by a fully symmetric protocol which uses a shared

variable on which only indivisible read and write operations are allowed. (This is impossible to achieve in either approach.)

It turns out that a major conceptual problem in the analysis of a probabilistic parallel program is the choice of the right model for the possible execution sequences of the processes. It is generally accepted paradigm that the processes can be viewed as executing in sequence (with no two processes ever executing simultaneously). At each step some process is chosen to perform the next 'atomic' action. The decisions which process will be chosen to operate next are thought of as being taken by some imaginary scheduler, and the main problem is: what rules can the scheduler use in its decisions. For example, the admissible schedules considered by Rabin in [Ral] are more restricted than those considered by Lehmann and Rabin in [LR]. We will show, as a consequence of our general theorems, that Rabin's algorithm for synchronization given in [Ral] does not have the required properties if one allows also schedules of the sort considered in [LR].

## 1. <u>Characterization of Probabilistic Concurrent</u> Termination.

The model that we use for representation of a probabilistic parallel program is the following: States in the program are identified by the location in each process and by the values of shared variables and local variables. With each process k we associate a transition probability matrix  $\mathbf{p}^{k}$ describing the possible state transitions that can occur as a result of a single atomic action of k , i.e.,  $p_{k}^{k}$  is the probability of reaching state j from <sup>1</sup> state i under one step of process k . Let I denote the set of all possible states. Let  $P^1, \ldots, P^S$  be the transition matrices of the processes  $1, \ldots$  s participating in such an asynchronous parallel program. An execution tree, also referred to as a schedule  $\sigma$  , is a tree constructed as follows: Nodes in the tree are pairs (i,k) where i  $\in$  I is a program state and  $k \in \{1, \ldots, s\}$  is the process scheduled to operate on that node. The root is labeled by  $(i_0,k)$  where  $i_0$  is the initial state. The

edges going out of a node labeled by (i,k) correspond to nonzero entries in the i-th row of P<sup>k</sup>, and designate transitions having positive probabilities from state i to other states under execution of process k. Thus an edge from (i<sub>1</sub>,k<sub>1</sub>) to (i<sub>2</sub>,k<sub>2</sub>) means that if process k<sub>1</sub>

is scheduled to operate on the state  $i_1$ , there is a positive probability to reach state  $i_2$ . The appearance of these nodes in the schedule  $\sigma$  implies that  $k_1$  is indeed scheduled at  $i_1$ 

and  $k_2$  at  $i_2$  if the execution actually gets

there. Note that the scheduler's decisions may depend on the whole path in the tree leading to a node, and so different processes may be scheduled on the same state in different nodes of the tree.

To ensure proper functioning of the parallel program, we will require that  $\sigma$  possesses certain "fairness" properties.

<u>Definition.</u> A schedule  $\sigma$  is called <u>strictly fair</u> if each path  $\Pi$  in the associated transition tree

is a fair path, i.e. each process  $k \in K$  labels infinitely many nodes of  $\ensuremath{\mathbb{N}}$  .

That is, in each execution of the program each process is scheduled infinitely often. This is a standard requirement (see [LR] for example, where this is called "proper") in analysis of parallel programs.

We will also consider a weaker notion of fairness. For this, note that a schedule  $\sigma$  induces a probability measure  $\mu = \mu_{\sigma}$  on the sequence

space whose elements are paths in the transition tree associated with  $\sigma$ . This measure is defined on the Borel field generated by all the finite cylinders (i.e. sets of paths having the same initial segment) such that the measure of such a cylinder is the product of probabilities of the edges of the common initial path.

<u>Definition</u>. A schedule  $\sigma$  is called <u>fair</u> if  $\mu_{\sigma}$ -almost every path in the associated transition tree is fair.

As will be shown below, fair schedules are in some sense limit points of strictly fair schedules, so that with no loss of generality, we could deal with either class of schedules, and obtain essentially the same results.

The main problem that we shall be concerned is the probability of reachability of certain states from other states. For example, if i is a state in which a process k is trying to enter its critical section, and X is the set of all states in which k is at its critical section, then the probability of reaching X from i is the probability that k will not be locked out of the set X at state i. Similarly, if  $X_1$  is the set of states in which <u>some</u> process is at its critical section, then the probability of reaching  $X_1$  from i is the probability that the system

will not be deadlocked at state i.

Let therefore  $X \subset I$  be a given set of 'goal' states, and let i  $\in$  I be an 'initial' state. Our aim is to compute

 $f_{i,X}^{*}(\sigma) \equiv \text{probability of ever reaching a state}$ in X from i under  $\sigma$ ,

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where  $\sigma$  is some fair schedule. Note that this can also be expressed as

$$f_{i,X}^{\star}(\sigma) = \sum_{n=0}^{\infty} f_{i,X}^{(n)}(\sigma) ,$$

where

$$\begin{array}{l} f_{i,X}^{(n)}(\sigma) = \mbox{probability of reaching a state in} \\ & \chi \quad \mbox{from } i \mbox{ for the first time} \\ & \mbox{after exactly } n \mbox{ steps under } \sigma \ . \end{array}$$

Since we will be interested in showing that

 $f_{i,X}^{*}(\sigma) = 1 \text{ for every fair schedule } \sigma, \text{ we define}$  $h_{i,X}^{*} = \inf\{f_{i,X}^{*}(\sigma) : \sigma \text{ a fair schedule}\}$ 

and we seek conditions under which  $h_{i,X}^{\star} = 1$ .

Let us first explain our reasoning intuitively. The way in which an adversary scheduler can prevent the system from ever entering X is to iterate forever through a set of states E disjoint from X, scheduling at each  $i \in E$  some process k which transfers i only to states in E, and using all processes in this manner. We will show that the nonexistence of such a set E is a necessary and sufficient condition for  $h_{i,X}^*$  to be l

We begin by a precise definition of the above notion:

<u>Definition.</u> A set  $E \subset I$  is called K-ergodic if the following holds: For each i  $\in E$  define

$$\kappa_{E}(i) \equiv \{k \in \kappa \mid P_{i,E}^{k} \equiv \sum_{j \in E} P_{ij}^{k} = 1\}$$

(this is the set of processes that cannot lead from i to a state outside E). Then we require that

- (i) U  $K_{E}(i) = K$ , i.e. for every process  $k \in K$ i $\in E$  there is a state i  $\in E$  such that all k transitions out of i are still in E.
- (ii) For every  $i \neq j \in E$  there exists a chain  $i_0, i_1, \dots, i_r \in E$  with  $i_0 = i$ ,  $i_r = j$ , and a chain  $k_0, k_1, \dots, k_{r-1} \in K$  of processes such that for each s = 0,  $1, \dots, r-1$

and

 $k_{s} \in K_{E}(i_{s})$ 

(a set E satisfying (ii) will be called communicating).

Let  $i \in I$  be the initial state, and  $X \subset I$ be the goal set with  $i \notin X$ . Define  $\hat{I}$  as the set of all states that can be reached (with positive probability) from  $\hat{i}$  before a state in X is reached using any finite sequence of processes.  $\hat{I}$  includes  $\hat{i}$  and is disjoint from X. Our main result is

<u>Theorem 1:</u> Let i, X, I be as above, and assume that  $\hat{I}$  is finite. Then the following conditions are equivalent:

- (1)  $h_{i,X}^{*} = 1$
- (2)  $h_{i,X}^* = 1$ , for each  $i \in \hat{I}$ .
- (3) I does not contain any K-ergodic set
- (4) There exists a decomposition of I into disjoint sets  $I_1, \ldots, I_n$ , with  $I_0 = X_n$ , such that if we put  $J_m = U I_r$ ,  $m = 0, 1, \ldots, n$ , then for each  $m = 1, 2, \ldots, n$  we have

(a) For each 
$$i \in I_m, k \in K$$
,  
 $P_{i,J_{m-1}}^k = 0 \Rightarrow P_{i,I_m}^k = 1$ 

(b) There exists  $k \equiv k(m) \in K$  such that for

each  $i \in I$   $P^k$  > 0.

(Condition (4.a) says that if process k can transfer the system from a state in I to a state outside I<sub>m</sub>, then some k-transitions [with non-zero probabilities) move the system 'down' the chain I<sub>r</sub>, towards the goal I<sub>0</sub>; Condition (4.b) ensures the existence of at least one process that would do this for all states in I<sub>m</sub>.)

The proof is technical and involved, and is therefore omitted in this version. Part of the proof, namely that (3) is equivalent to (4), is implied by the algorithm below.

## Remarks.

(1) We can regard the partition of I into I<sub>1</sub>,...,I<sub>n</sub> as the assignment of an element of the well founded set {1,...,n} to each state i  $\in$  I. That is, each i  $\in$  I<sub>m</sub>, 1  $\leq$  m  $\leq$  n is assigned the value m. Denote this assignment by  $\rho: I \rightarrow$  {1,...,n}.

Condition (4.a) then states that for every i and k, if there is no k transition leading to i' such that  $\rho(i) > \rho(i')$ , then all k transitions must lead to i's such that  $\overline{\rho(i)} = \rho(i')$ .

This view shows our method to be an extension of the proof method given in [LPS] for the fair termination of deterministic concurrent programs. Indeed, if we restrict ourselves to deterministic processes (i.e., assume that for any  $i \in I$ ,  $k \in K$ ,

 $^k_{j\,j}$  = 1 for exactly one  $j \in I$  ), then our condition 4 reduces to the proof method given in [LPS]. An open question is whether our results can be extended to processes with infinite state sets.

(2) An immediate corollary of the preceding theorem is that the convergence is independent of the particular values of nonzero transition probabilities.

(3) It is also easy to show that, under the assumptions of the preceding theorem,  $\min_{i,X} h_{i,X}^{i \in \hat{1}}$  is either 0 or 1. We also show that the preceding theorem remains true if one admits only strictly fair schedules.

Next we present an algorithm that, given the set  $\hat{1}$  of intermediate states, the set X of goal states, and the transition probability matrices of the processes, either constructs a K-ergodic set  $E\subset\hat{1}$ , thereby showing that  $h_{\hat{1},X}^{\star} < 1$ , or else builds up the decomposition {I} as in the statement of the theorem, thereby showing that  $h_{\hat{1},X}^{\star} = 1$ .

The algorithm proceeds as follows:

(a) Construct a transition graph G, which is a directed labeled graph whose nodes are elements of  $\hat{I}$ , with one additional node designating X; for each  $k \in K$ , each  $i \in \hat{I}$  and  $j \in \hat{I}$  (or j = X) such that  $P_{i,j}^{K} > 0$ , we draw an edge e from i

to j and label e by k.

(b) We begin to construct a decomposition {I  $_{\rm M}$  by putting I  $_{\rm O}$  = X .

(c) Suppose that  $I_0, \ldots, I_r$  have already been constructed. Delete from 'G all nodes belonging to the union  $J_r = \bigcup_r I_k$  of these sets; also for k=0each i  $\in I \\ J_r$  delete from G all edges e such that either e leads from i to some  $j \in J_r$ , or there exists another edge e' leading from i to some node in  $J_r$ , and e and e' are labeled by the same process k  $\in K$ . (In other words, we ignore states that have already been assigned a rank, and transitions from a state i by processes that can, in a possibly alternative transition from state i, move the system into  $J_r$ .) Let  $G_r$ denote the resulting graph.

(d) Partition  $G_r$  into strongly connected components, and let E be such a component having no successors (terminal component). If each process k  $\in$  K labels some internal edge of E, then E is a K-ergodic set, in which case the algorithm halts, having found such a set; otherwise, put  $I_{r+1} \cong E$ , define k(r+1) to be a process k which does not label any internal edge of E, and repeat steps (c) and (d).

(e) If finally G empties out, then we have found the desired decomposition of  $\ \mbox{I}$  .

<u>An example.</u> Consider the following two-process synchronization, where a shared variable c is used (we assume indivisible 'test and set' operations on c). c has 3 values: 0 - designating a neutral state, 1 - a state in which process  $k_1$  will enter its critical section, and 2 - where  $k_2$  will enter. The code for  $k_1$  (resp.  $k_2$ ) is as follows:

Here we have 5 states, each represented as  $(c, \ell_1, \ell_2)$ , where c is the value of the shared variable, and where  $\ell_1$  (resp.  $\ell_2$ ) is the location in process  $k_1$  (resp.  $k_2$ ), which can

assume the values T (for being at the trying section) and X (for being in the critical section). We assume that both processes remain live indefinitely. The states are

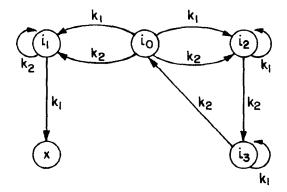
$$i_0 = (0, T, T)$$
  
 $i_1 = (1, T, T)$   
 $i_2 = (2, T, T)$   
 $i_3 = (2, T, X)$   
 $X = i_4 = (1, X, T)$ 

and the transition matrices are

	i <sub>0</sub>	il	1 <sub>2</sub>	i <sub>3</sub>	Х	i <sub>0</sub>	il	i <sub>2</sub>	i <sub>3</sub>	х
i <sub>0</sub>		$\frac{1}{2}$	$\frac{1}{2}$				$\frac{1}{2}$	$\frac{1}{2}$		
i.					1		1			
$i_2$			1						1	
i <sub>3</sub>				1		l 1				

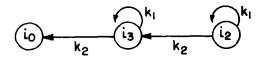
We will show that  $I = \{i_0, i_1, i_2, i_3\}$  does not contain a K-ergodic set by applying the algorithm described above. Begin by putting  $I_0 = X = \{i_4\}$ . The other sets are found as follows:

We first construct G :



(each edge is labeled by the process that induces it).

To find  $I_1$ , delete the  $k_1$  - edge from  $i_1$  to X to obtain  $G_0$ . A terminal strongly connected component in  $G_0$  is  $\{i_1\}$ , and only  $k_2$  labels internal edges of this component. Hence, we put  $I_1 = \{i_1\}$ ,  $k(1) = k_1$ . To find  $I_2$ , delete from  $G_0$  the node  $i_1$ , and all the edges going out of  $i_0$  (some of these edges lead to  $I_1$ , while the other edges are siblings of such edges, in the sense defined above.)  $G_1$  thus has the following form



from which one easily obtains  $I = \{i_0\}$ ,  $k(2) = k_1$ ,  $I_3 = \{i_3\}$ ,  $k(3) = k_2$  and  $I_4 = \{i_4\}$ ,  $k(4) = k_2$ .

We emphasize again that this construction and its consequences do not depend on the particular values of nonzero transition probabilities used in the randomization statements in the processes involved.

# 2. Probabilistic vs. Deterministic Protocols.

In this section we compare the capabilities of probabilistic protocols with those of deterministic protocols. An example motivating this study is an algorithm, given in [Ral], for synchronization of N processes, which uses only O(log N)-valued shared variable on which indivisible test-and-set operations are allowed. On the other hand, it is shown in [BFJLP] that in any deterministic synchronization algorithm for N processes using test-and-set operations on a shared variable, and providing both mutual exclusion and freedom from lockout, the shared variable must have about  $\sqrt{2N}$  values, and this lower bound increases to about N/2 values if certain natural constraints are imposed on the structure of the processes involved. The algorithm given in [Ral] is shown to provide mutual exclusion and to be lockout-free. However, the schedules under which this algorithm is assumed to operate form a restricted subset of the general fair schedules that we consider. It is helpful in this regard to ignore the 'demonic' nature of a schedule, and simply interpret it as an integral part of the execution tree. Thus the schedule does not really 'use' the past history of an execution to determine the next process to be scheduled, but simply records at each execution step which process is next ready to perform an atomic action. Other families of schedules considered in the literature impose certain constraints on the scheduling, e.g. bounded speed ratios between processes [RS], or, as is the case in [Ral], allowing the schedule to 'base its decisions' only upon partial information concerning past execution history, so that it must make the same scheduling choice for all sequences having the same partial structure. Such a restriction on the behavior of the schedule can improve the performance of a synchronization algorithm, but may make it less robust, in the sense that it may fail to meet some of its requirements if more general (fair) schedules are allowed.

We show in this section that Rabin's algorithm is not lockout-free if general fair schedules are allowed. As it turns out, this fact does not depend on the particular form of the algorithm, but follows from a general lower bound on the size of a shared variable necessary to ensure both mutual exclusion and freedom from lockout. Specifically, we show that the lower bounds given in [BFJLP] are also required for probabilistic synchronization algorithms of the same kind. This will follow from a generalization of the proofs given in [BFJLP] to the probabilistic case. Thus any algorithm attempting to use fewer values for such a shared variable is doomed to failure against general fair schedules, although it may still be successful against 'weaker' schedules, as is indeed the case in [Ral].

These results can be summarized as follows:

<u>Proposition 1:</u> The algorithm in [Ral] is almost surely lockout-free against the restricted family of (fair) schedules introduced there, but fails to have this property if general fair schedules are allowed.

Indeed, an "adversary" scheduler may base its decisions on values obtained by random assignments. Thus a program based on the assumption that if a variable in a process is repeatedly assigned a random value, and a certain value of this choice will ensure admittance into a critical section, if scheduled immediately then eventually the process will be admitted - such a program may fail under a generally "adversary" scheduler. This is so since the scheduler may observe the randomly chosen value and schedule the process only when such scheduling will not enable the process to access its critical section. Such scheduling is still fair with probability 1.

This is not the case with Rabin's scheduler whose decisions may not depend on randomly assigned values. Consequently a program correct under Rabin's scheduler may still fail under generally fair scheduler as defined here.

<u>Theorem 2:</u> Suppose that N processes participate in a synchronization protocol using a shared variable v on which indivisible test-and-set operations are allowed. Then at least  $\sqrt{2N}$  values of v are required to ensure both mutual exclusion and freedom from lockout. Furthermore, if each process has only a single state while being in its idle section (i.e. if it cannot remmeber any past experience), then at least (N+1)/2 values for v are required.

Proof: A generalization of the proofs given in [BFJLP].

The results stated so far in this section raise the general question: Under what circumstances does randomization really help us to obtain 'better' algorithms? The above negative result shows that we cannot hope to save space by incorporating randomization into synchronization protocols of the kind discussed in [BFJLP] and [Ral] (if we admit general fair schedules). We will next give another negative results. Consider a synchronization process involving  $N \ge 2$  identical processes, all of which use a common variable v which they may read from, or write into, in one indivisible step (all processes access v in exactly the same manner). It is well known that no deterministic solution to this problem, that provides mutual exclusion, can exist. Surprisingly enough, an analogous proof shows that a similar result holds in the probabilitisticcase as well:

<u>Theorem 3:</u> There does not exist a probabilistic fully symmetric protocol that solves the above synchronization problem, and ensures both mutual exclusion and freedom from lockout with probability 1.

<u>Proof:</u> Suppose that there exists such a protocol. We assume that no lockout can occur with positive probability. Hence, if we start from a symmetric configuration i in which all processes are idle, and we let only one process (say  $k_1$ ) execute by itself and leave the other processes in their idle state,  $k_1$  must eventually enter its critical section with probability 1. This implies that there exists at least one finite execution path  $\Pi$  in which  $k_{\rm c}$  executes alone, and all other processes remain idle, such that all transitions on  $\Pi$  have positive probabilities and such that  $k_{\rm c}$  gets via  $\Pi$  from its idle section to its critical section. Let the product of the transition probabilities along  $\Pi$  be p > 0.

Let us now execute the protocol as follows. Start at the symmetric configuration i , and assume that all processes become active (i.e. get out of the idle section) together. Schedule processes in a round robin fashion, letting each of them perform one atomic action at its turn. We will construct a path (having positive probability) such that at the end of each round, the configuration is again symmetric. Assume that this were the case at the beginning of some round r. Then each process is at the same internal state and so performs the same action in its turn. If this action does not involve randomization, then either all the processes in this round manipulate their internal variables in the same way, or they all read the (same value of the) shared variable, or they all write the same value into the shared variable. Hence, in either case, their final configuration is again symmetric. Note that in this mode of scheduling, each process is not aware that other processes are also executing, so that each process will follow a sequence of states identical to those that  $k_1$  passed by itself along the sequence II (as long as there is not randomization) Now suppose that the common action of the processes in round r did involve randomization. Then with some positive probability  $\rm p > 0$  ,  $\rm k_1$  will make the choice that will keep it in the sequence  $\rm ~I\!I$  , and with the same probability each other process will make an identical choice. Consequently, with probability  $P_{\rm }^{\rm N}$  (where N is the number of processes) all processes would make this common choice, and will again reach a symmetric configuration; moreover, in this new configuration each process is unaware of the existence of the other processes, and its state is the symmetric image of the r-th state of  $\,k_{1}^{}\,$  on  $\,\Pi$  .

Continuing in this manner, with probability  $p^N > 0$  all processes would enter their critical sections together after I steps, since  $k_1$  does so and the behavior of the other processes remains symmetric to that of  $k_1$  under this particular execution sequence. This contradicts our assumptions, and so proves the theorem.

Q.E.D.

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